

**Search Notes**

Application/Control No.

10/736,657

Examiner

Peter Coughlan

Applicant(s)/Patent under  
Reexamination

LOPEZ-ESTRADA, ALEX A.

Art Unit

2129

**SEARCHED**

| Class | Subclass | Date      | Examiner |
|-------|----------|-----------|----------|
| 706   | 15       | 10/5/2006 | PDC      |
| 706   | 21       | 10/5/2006 | PDC      |
| 706   | 23       | 10/5/2006 | PDC      |
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**INTERFERENCE SEARCHED**

| Class | Subclass | Date | Examiner |
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**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

|   | DATE      | EXMR |
|---|-----------|------|
| East--ospf, jpeg, 3des, aes, ip packet, performance monitoring, cpu, platform, configuration, parameters, patterns, table,    | 10/5/2006 | PDC  |
| East--II--pre=determined, execution, input data, table, policy manager, clocktick, instructions retired, bus access, l2 cache | 10/5/2006 | PDC  |
| East-III--load intructions, mispredicted branches, branches retired, read operations, write operations, trace cache, buffer   | 10/5/2006 | PDC  |
| IEEE--ospf, jpeg, 3des, aes, ip packet, performance monitoring, cpu, platform, configuration, parameters, patterns, table,    | 10/5/2006 | PDC  |
| IEEE-II--load intructions, mispredicted branches, branches retired, read operations, write operations, trace cache, buffer    | 10/5/2006 | PDC  |
| IEEE--III--pre=determined, execution, input data, table, policy manager, clocktick, instructions retired, bus access          | 10/5/2006 | PDC  |
| Invetor<br>Lopez-Estrada Alex   | 10/5/2006 | PDC  |
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